

Atty Docket No.: JCLA5312

Serial No.: 09/853,005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Patent Application of :

Chau-Chad Tsai ;
Chi-Che Tsai ;
Chen-Ping Yang

Examiner : BAKER, PAUL A

Serial No. : 09/853,005

Art Unit : 2187

Filed : May 09, 2001

Atty Docket No. : JCLA5312

For : **PERIPHERAL DEVICE INTERFACE
CHIP CACHE AND DATA
SYNCHRONIZATION METHOD**

No fee is believed to be due. However, the Commissioner is authorized to charge any fees required in connection with the filing of this paper to account No. 50-0710 (Order No. JCLA5312).

RESPONSE TO OFFICE ACTION

Assistant Commissioner of Patents and Trademarks
Washington, DC 20231

Dear Sir:

In response to an Office Action having a mailing date of Sep. 20, 2002, Applicants respectfully request the Office Action to consider the following remarks.

REMARKS**Present Status of the Application**

In response to the above-identified Office Action, Applicants seek reconsideration of the application. No claims have been canceled, no claims have been added and no claims have been amended. Accordingly, Claims 1-16 are pending.

In the Office Action, the Office Action has rejected Claims 1, 2, 4-6 under 35 U.S.C. §102(e) as being anticipated by Ziegler et al US Patent 6,182,176 ("Ziegler").

#6

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CERTIFICATE OF TRANSMISSION

December 18, 2002

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BY FACSIMILE ONLY

Fax No. : 703-746-7239
Attention : Examiner BAKER, PAUL A.
Group Unit : 2187
From : Jiawei Huang, Reg. No. 43,330
MESSAGE : Enclosed is an Amendment in 14 pages.

Sir:

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office on December 18, 2002 at the above indicated fax number.

Sign by: M. Chang
Michelle Chang

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